

Fig.1a

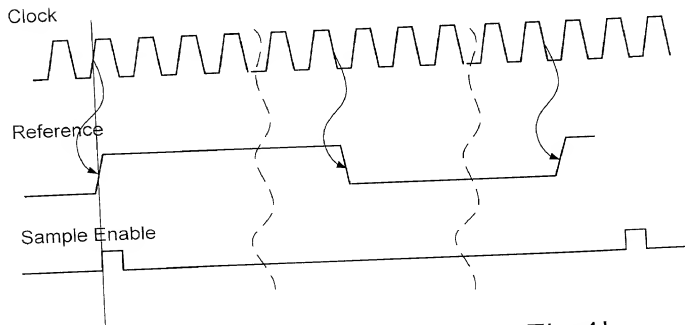


Fig.1b

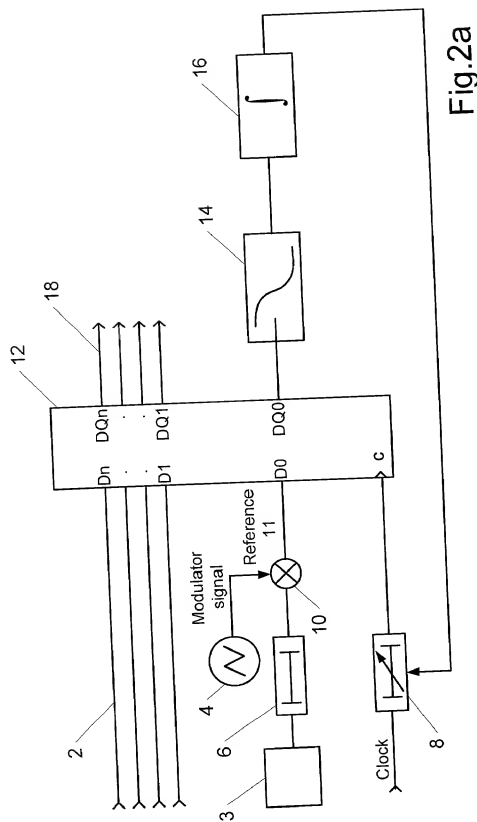


Fig.2a

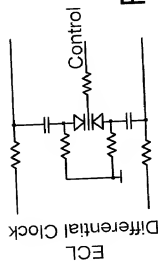


Fig.2b



Fig.2c

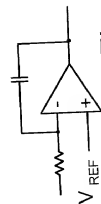


Fig.2d

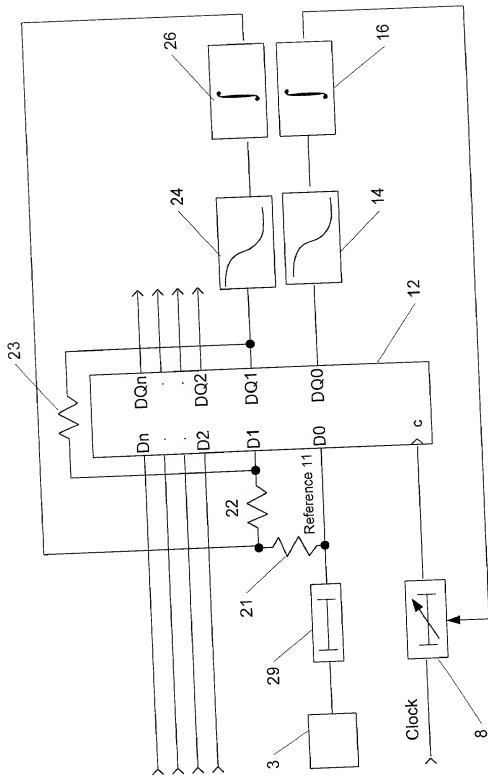
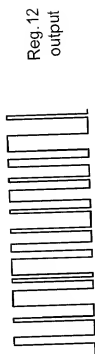
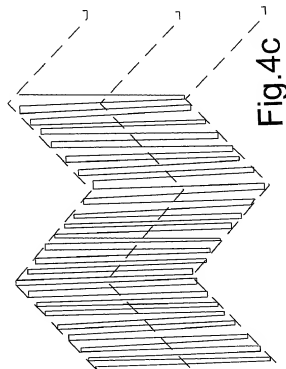
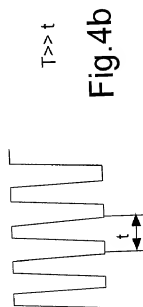
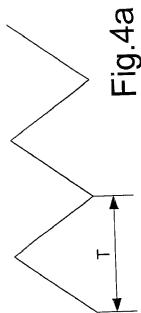
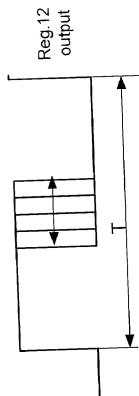


Fig.3



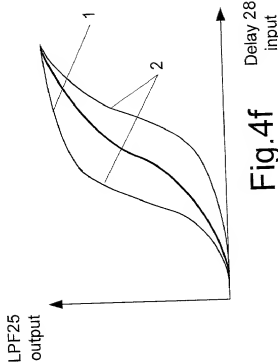
Reg. 12  
output

Fig. 4d



Reg. 12  
output

Fig. 4e



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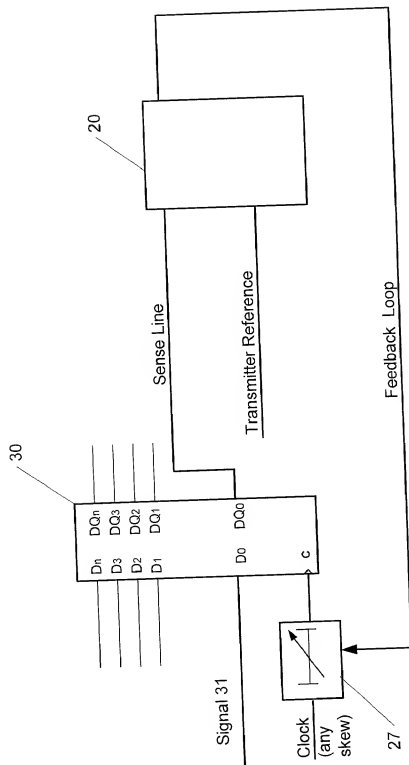


Fig.5

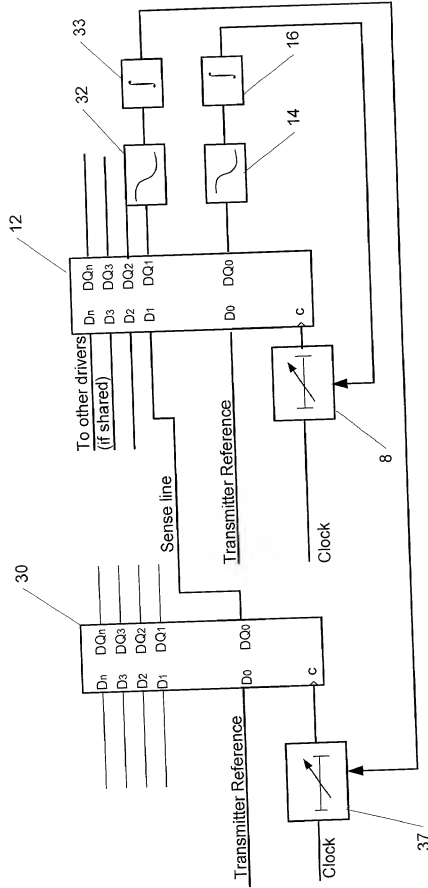


Fig.6

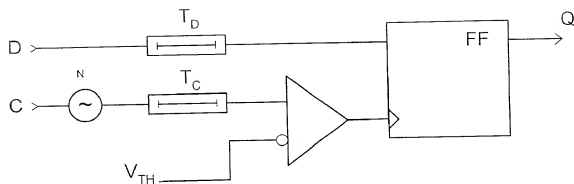


Fig.7a

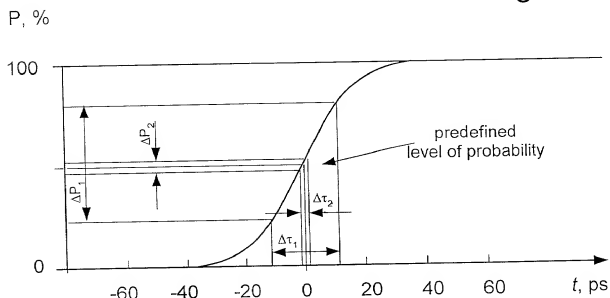


Fig.7b

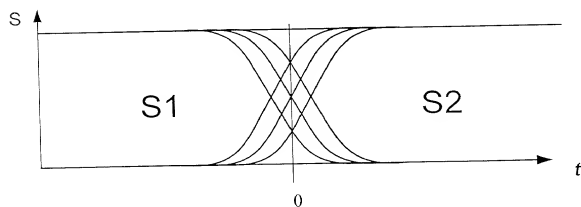


Fig.7c

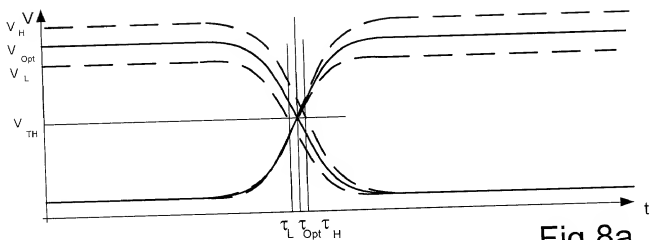


Fig.8a

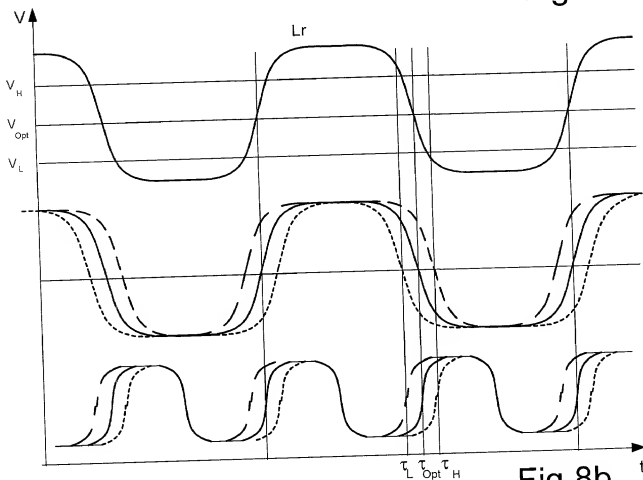


Fig.8b

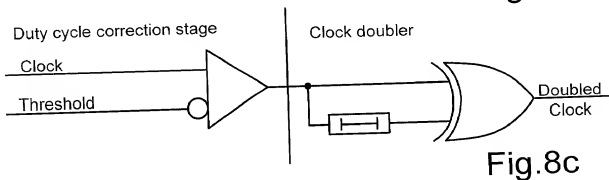


Fig.8c



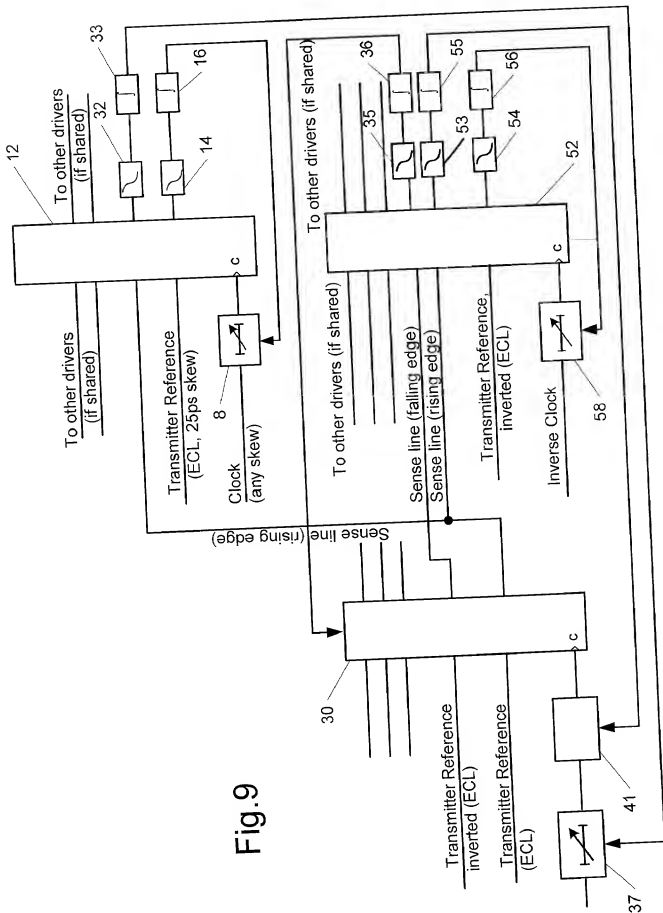
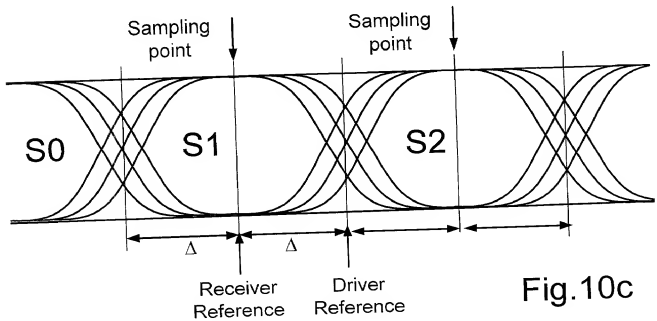
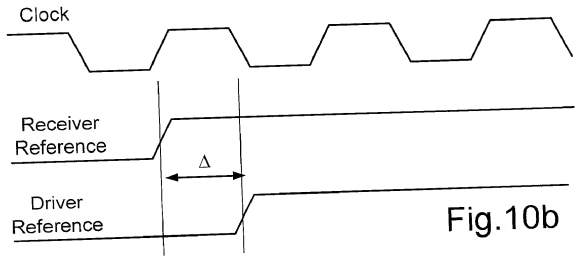
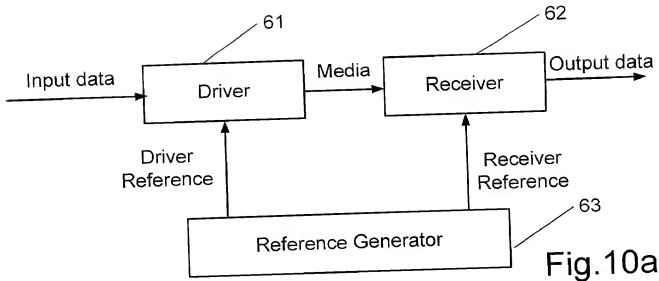


Fig. 9

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